Please rewrite the paragraph at page 2, lines 7-11 as follows:

A 1

In step S1, CPU 5 receives a supply of image data corresponding to one frame from video camera 1. This image data, which can be considered as the source frame image data, consists of an HMAX, VMAX array of pixel data C_{sp} as shown, for example, in Fig. 3. In step S1, CPU 5 also sets the convolution filter coefficients $C_v[m][n]$. In the example in Fig. 4, these convolution filter coefficients $C_v[m][n]$ consist of a 3x3 array of coefficients.

REMARKS

Claims 1-27 are pending in the application.

Claims 1-27 are rejected under 35 U.S.C. § 103 as being unpatentable over the applicant's admitted prior art (figs. 1-5, pages 1-5 of the specification) in view of Penna et al. (5,838,332).

The Examiner relied on element 11 as being a first storage means for storing source image data in claims 1,8,9 and element 21 (Fig. 1) as being a second storage means for storing destination image data.

The Examiner has also indicated that rendering means for performing an action of applying a stimulated pixel-unit operation to the source image data stored in the first storage means and rendering the data as destination image data in the second storage means in units repeatedly until a stipulated arithmetic result is obtained is also disclosed in the admitted prior art and the only difference between the claimed invention and the AAPA (Applicant's Admitted Prior Art) is that the rendering data is not in units of polygons.

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